

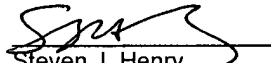
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Guido Retz and David Philip Burton
Serial No.: 10/600,986
Confirmation No.: 7134
Filed: June 20, 2003
For: VARIABLE GAIN AMPLIFIER

Examiner: --
Art Unit: 2817

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the 4th day of November, 2003.


Steven J. Henry
Reg. No. 27,900

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

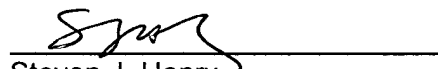
Transmitted herewith are the following documents:

- [x] Information Disclosure Statement, PTO-1449 and references
- [x] Return Receipt Postcard

If the enclosed papers are considered incomplete, the Mail Room and/or the Application Branch is respectfully requested to contact the undersigned at (617) 720-3500, Boston, Massachusetts.

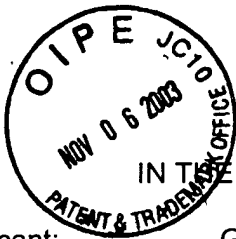
No check is enclosed to cover the filing fee. If the fee is insufficient, the balance may be charged to the account of the undersigned, Deposit Account No. 23/2825. A duplicate of this sheet is enclosed.

Respectfully submitted,


Steven J. Henry
Reg. No. 27,900
Wolf, Greenfield & Sacks, P.C.
600 Atlantic Avenue
Boston, MA 02210-2211
(617)720-3500

Docket No.: G0631.70035 US00
Date: November 4, 2003

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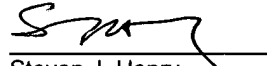
ATTORNEY DOCKET NO.: G0631.70035 US00

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Steven J. Henry
Reg. No. 27,900

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

STATEMENT FILED PURSUANT TO THE DUTY OF

DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

PART I: Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement has been filed before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. §1.114.

No fee or certification is required.

PART II: Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

PART III: Remarks

Documents cited anywhere in the Information Disclosure Statement are enclosed unless otherwise indicated. It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

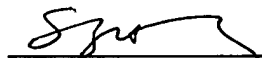
By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,



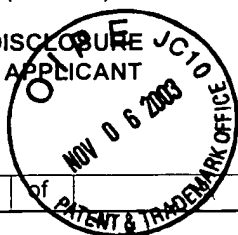
Steven J. Henry
Reg. No. 27,900
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(617)720-3500

Docket No.: G0631.70035 US00

Date: November 4, 2003

xndd

FORM PTO-1449/A and B (Modified)		APPLICATION NO.: 10/600,986	ATTY. DOCKET NO.: G0631.70035 US00
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE: June 20, 2003	CONFIRMATION NO.: 7134
		APPLICANT: Guido Retz and David Philip Burton	
Sheet	of	GROUP ART UNIT: 2817	EXAMINER: --



U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
		6,218,899		Ezell	04-17-2001

FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/Country	Number	Kind Code			

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
		"MAX3524, Low-Noise, High-Linearity Broadband Amplifier", Maxim Integrated Products, 7/00, pp. 1-7.	
		"A CATV Attenuator Using the Single Package SMP1307-027 PIN Diode Array", Alpha Industries, Inc., 11/99, pp. 1-10.	
		Kaunisto, Risto, et al, "A Linear-Control Wide-Band CMOS Attenuator", Helsinki University of Technology, 9/01, pp. IV-458-461.	
		Meyer, Robert G. et al, "A DC to 1-GHz Differential Monolithic Variable-Gain Amplifier", IEEE Journal of Solid-State Circuits, Vol. 26, No. 11, 11/91, pp. 1673-1680.	
		Otake, Shoji et al, "A Low-Power Low-Noise Accurate Linear-in-dB Variable-Gain Amplifier with 500-MHz Bandwidth", IEEE Journal of Solid-State Circuits, Vol. 35, No. 12, 12/2000, pp. 1942-1948.	
		Yamaji, Takafumi et al, "A Temperature-Stable CMOS Variable-Gain Amplifier with 80-dB Linearly Controlled Gain Range", IEEE Journal of Solid-State Circuits, Vol. 37, No. 5, 5/2002, pp. 553-558.	
		Connell, Larry et al, "A CMOS Broadband Tuner IC", Motorola, Inc., ISSCC 2002, pp. 324-325, 529.	
		"AD8367, 500 MHz, Linear-in-dB VGA with AGC Detector", Analog Devices, Inc., 2001, pp. 1-16.	

EXAMINER	DATE CONSIDERED
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. __, filed __, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

[NOTE - Must provide a copy of any patent, publication, other information listed, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]